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## FACSIMILE COVER SHEET

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kowalski, et al. Docket No.: INF-125  
 Serial No: 10/748,332 Art Unit: 2811  
 Date Filed: December 31, 2003  
 TITLE: Device Architecture and Process for Improved Vertical Memory Arrays

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Respectfully submitted,

*Natalie Swider*  
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Confirmation Respectfully Requested

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